

<b>Notice of References Cited</b>		Application/Com. No.	Applicant(s)/Patent Under Reexamination ARAI, TAKAO	
		09/963,533	Examiner	Art Unit
Johannes P Mondt		2826	Page 1 of 1	

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C	US-5,424,226	06-1995	Vo et al.	437/30
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L	US-			
M	US-			

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	S. Wolf, "Silicon Processing for the VLSI Era", Volume 2 - Process Integration, Lattice Press, Sunset Beach, California, USA (1990), in particular pages 383-389.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.